

[Aĭzerman, M. A.](#); [Gusev, L. A.](#); [Rozonoër, L. I.](#); [Smirnova, I. M.](#); [Tal', A. A.](#)

Clock conversion in sequential machines and the synthesis of relay networks. (English. Russian original) [Zbl 0135.18204](#)

[Autom. Remote Control](#) 23, 1376-1398 (1963); translation from [Avtom. Telemekh.](#) 23, 1465-1491 (1962).

Summary: An algorithm is proposed for constructing a minimal-state sequential machine operating at a certain ("fast") clock rate and reproducing a given sequential machine which operating at a rate defined by changes in the input state. The algorithm may be used for the synthesis of relay networks.

For a scan of this review see the [web version](#).

MSC:

[93C83](#) Control/observation systems involving computers (process control, etc.)

[94C05](#) Analytic circuit theory

Full Text: [MNR](#)