Semelectivity is not sufficient. (English) Zbl 0714.68047

Summary: We prove that semelective VLSI circuits are more powerful than VLSI circuits satisfying both semelectivity and time-determinate input assumption regarding area complexity.

MSC:
68W35 Hardware implementations of nonnumerical algorithms (VLSI algorithms, etc.)
68Q15 Complexity classes (hierarchies, relations among complexity classes, etc.)
03D15 Complexity of computation (including implicit computational complexity)

Keywords:
computational complexity; VLSI model; lower bound; area complexity

Full Text: DOI

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