

Herendi, Tamás; Major, Roland Sándor

Modular exponentiation of matrices on FPGA-s. (English) Zbl 1297.65049

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MSC:

65F60 Numerical computation of matrix exponential and similar matrix functions

11Y55 Calculation of integer sequences

Keywords:

pseudo random number generators; linear recurring sequences; uniform distribution; matrix exponentiation; parallel arithmetic; FPGA design; hardware acceleration of computations; hardware implementation of computations