Napoli, Ettore; Castellano, Gerardo; De Caro, Davide; Esposito, Darjn; Petra, Nicola; Strollo, Antonio G. M.

A SISO register circuit tailored for input data with low transition probability. (English)


Editorial remark: No review copy delivered.

MSC:
- 68M07 Mathematical problems of computer architecture
- 68W35 Hardware implementations of nonnumerical algorithms (VLSI algorithms, etc.)

Full Text: DOI