Wasaki, Katsumi
Stability of the 7-3 compressor circuit for Wallace tree. I. (English) [Zbl 1462.68230]


MSC: 68W20 Formalization of mathematics in connection with theorem provers 68M07 Mathematical problems of computer architecture 68W35 Hardware implementations of nonnumerical algorithms (VLSI algorithms, etc.)

Keywords: arithmetic processor; high-order compressor; high-speed multiplier; Wallace tree; logic circuit stability

Software: Mizar

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References:


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